

Fig. 13 is a flowchart illustrating one embodiment of a method.

On page 32, line 11, please insert the following paragraph:

Turning now to Fig. 13, a flowchart is shown illustrating one embodiment of a method. At block 400, an operating mode is established in a processor. The operating mode is established in response to an enable indication in a control register within the processor, a first operating mode indication in a segment descriptor, and a second operating mode indication in the segment descriptor. For example, a first operating mode may be established responsive to the enable indication being in an enabled state and the first operating mode indication being in a first state, wherein the first operating mode includes a default address size greater than 32 bits. As another example, a second operating mode may be established responsive to the enable indication being in an enabled state, the first operating mode indication being in a second state, and the second operating mode being in the first state, wherein the second operating mode includes a default address size of 32 bits. As yet another example, one of a plurality of operating modes may be established if the enable indication is in the enabled state and the first operating mode indication is in a second state, wherein the one of the plurality of operating modes is selected in response to a state of the second operating mode indication. At block 402, the processor fetches operands and generates addresses in response to the operating mode.

IN THE CLAIMS:

Please amend claim 21. Set forth below is a clean copy of claim 21 after entry of this amendment. Included herewith is a marked-up copy of claim 21, illustrating the changes made thereto in this amendment.

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21. (Amended) The method as recited in claim 18 wherein said establishing further comprises establishing a second operating mode responsive to said enable indication